

STMI07-02357

COPC

**PATENT** 

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No. :

Customer No.: 23990

7,172,980 B2

Inventor(s)

Joaquin Torres et al.

Issued

February 6, 2007

Title

PROCESS FOR FABRICATING AN INTEGRATED

ELECTRONIC CIRCUIT THAT INCORPORATES AIR GAPS

Certificate of Correction Branch Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Certificate

MAY 0 3 2007

of Correction

## CERTIFICATE OF MAILING BY FIRST CLASS MAIL

Sir:

The undersigned hereby certifies that the following documents:

- 1. Request for Certificate of Correction (in duplicate);
- 2. Certificate of Correction (in duplicate); and
- 3. A postcard receipt

Date:

27, 2007

Date:

Mailer

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E-mail: wmunck@munckbutrus.com

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### **REQUEST FOR CERTIFICATE OF CORRECTION**

Certain clerical or typographical errors were noted in the above-identified patent as indicated on the attached Certificate of Correction.

Applicant respectfully requests that a Certificate of Correction be issued pursuant to 37 C.F.R. §1.322. The Commissioner is hereby authorized to charge any fees associated with this communication or credit any overpayment to Deposit Account Number 50-0208.

MAY :- 7 2007

# ATTORNEY DOCKET No.: STMI07-02357 PATENT No.: 7,172,980 B2

Respectfully submitted,

MUNCK BUTRUS, P.C.

Date: April 37, 3007

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PATENT NO.

: 7,172,980 B2

DATED

: February 6, 2007

INVENTOR(S) : Joaquin Torres, Vincent Arnal and Laurent Gosset

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4, line 60, delete "image processing system" and replace with -- integrated circuit or semiconductor processing system --.

MAILING ADDRESS OF SENDER:

PATENT NO. 7,172,980 B2

**Docket Clerk** 

P.O. Drawer 800889

No. of additional copies

Dallas, Texas 75380

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DUPLICATE

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